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Terms	Documents
L2 same controller	36

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DB=PGPB,USPT,USOC; PLUR=YES; OP=OR			
L3	L2 same controller	36	L3
L2	L1 same (chip or IC)	75	L2
L1	(memory adj1 (card or board)) same (interfac$3 near5 (USB or "universal serial bus"))	879	L1

serial

END OF SEARCH HISTORY

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L4	L3	0	L4
DB=PGPB,USPT,USOC; PLUR=YES; OP=OR			
L3	L2 same controller	36	L3
L2	L1 same (chip or IC)	75	L2
L1	(memory adj1 (card or board)) same (interfac$3 near5 (USB or "universal serial bus"))	879	L1

X serial (handwritten annotation)

END OF SEARCH HISTORY

- Drafts
- Pending
- **Active**
  - L1: (170) (memory adj1
  - L2: (15) l1 same (chip
  - L3: (7) l2 same control
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1	BRS	L1	170	(memory adj1 (card or board)) same (interfa	USPA T	2005/08/2 2 17:07				
2	BRS	L2	15	l1 same (chip or IC)	USPA T	2005/08/2 2 17:07				
3	BRS	L3	7	l2 same controller	USPA T	2005/08/2 2 17:07				

Start   EAST - [...

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- Pending
- Active
  - L1: (170) (memory adj1
  - L2: (15) l1 same (chip
  - L3: (7) l2 same control
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l2 same controller

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	U	I	Document ID	Issue Dat	Pages	Title	Current OR	Current XR
1	☐	☐	US 6912638 B2	20050628	22	System-on-a-chip controller	711/167	345/534; 345/571
2	☐	☐	US 6854984 B1	20050215	19	Slim USB connector with spring-engaging depress	439/79	439/610
3	☐	☐	US 6839864 B2	20050104	33	Field-operable, stand-alone apparatus f	714/5	711/159
4	☐	☐	US 6737877 B1	20040518	11	Method and circuit for reading a potentiometer	324/723	324/535; 324/677;
5	☐	☐	US 6725286 B2	20040420	21	Information-processing apparatus, information-	710/8	710/11; 710/62;
6	☐	☐	US 6438638 B1	20020820	22	Flashtoaster for reading several types o	710/301	710/303
7	☐	☐	US 6246578 B1	20010612	8	Computer-dedicated auxiliary data access d	361/686	361/724; 361/726;

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IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

Select   Article Information

☐   1. **The performance improvement of a photo card reader by the use of a high-integration chip solution with double FIFO buffers**
Ying-Wen Bai; Chang-Chih Liu;
Consumer Electronics, IEEE Transactions on
Volume 51, Issue 2, May 2005 Page(s):329 - 334
Digital Object Identifier 10.1109/TCE.2005.1467967

AbstractPlus | Full Text: PDF(655 KB)   IEEE JNL

☐   2. **Memory on the move**
Sherwin, R.M.;
Spectrum, IEEE
Volume 38, Issue 5, May 2001 Page(s):55 - 59
Digital Object Identifier 10.1109/6.920032

AbstractPlus | Full Text: PDF(264 KB)   IEEE JNL

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The performance improvement of a photo card reader by the use of a high-integration chip solution with double FIFO buffers

Ying-Wen Bai   Chang-Chih Liu
Dept. of Electron. Eng., Fu Jen Catholic Univ., Taipei, Taiwan

Abstract

The insufficient bandwidth of SDRAM access has created a bottleneck in the performance of displaying and processing when used in previous design of the photo card reader. In this paper, we propose three ways to overcome this drawback. First, we double the clock rate of the SDRAM operation to increase the amount of the memory bandwidth. Second, we use a dual port design of the SDRAM with a double buffer for the strip module to increase the usage efficiency of the bandwidth. Third, we also use a double buffer for the mem/spl l.bar/ctrl module to increase the usage efficiency of the bandwidth. Using the extra gate counts of double FIFO buffers results in an increase of 3.3% from the previous system. Our new design has an improvement in the processing speed of about 4.4 times for displaying photos.

Index Terms

Inspec

Controlled Indexing
SRAM chips   buffer storage   digital photography   memory cards   smart cards

Non-controlled Indexing
SDRAM   clock rate   double FIFO buffer   dual-port design   high-integration chip solution   photo card reader
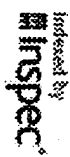
Author Keywords
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References
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